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Sheet 1 of 2

Complete if Known	
Application Number	Unknown
Filing Date	Herewith
First Named Inventor	John M. RUDOSKY
Group Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	021202-100100US

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	U.S. Patent Document Number	Kind Code[2] (*if known*)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Office[3]	Number[4]	Kind Code[5] (*if known*)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known	
Application Number	Unknown
Filing Date	Herewith
First Named Inventor	John M. RUDOSKY
Group Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	021202-100100US

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
SHS	C1	George C. Clark, Jr., and J. Bibb Cain, Error Correction Coding for Digital Communications, Plenum Press, New York, 1981.	
SHS	C2	Hekstra, Andries P., "An Alternative to Metric Rescaling in Viterbi Decoders", IEEE Transactions on Communications, Vol. 37, No. 11, Nov 1989.	
SHS	C3	E. Yeo, S. Augsburger, W. R. Davis, and B. Nikolic, "Implementation of High Throughput Soft Output Viterbi Decoders," Proc. IEEE Workshop on Signal Processing Systems, , pp. 146-151, San Diego, CA, Oct 16-18, 2002.	
SHS	C4	M. Bickerstaff, et al., "A Unified Turbo/Viterbi Channel Decoder for 3GPP Mobile Wireless in 0.18□m CMOS", in IEEE Journal of Solid-state Circuits, Vol. 37, No. 11, November 2002 pg. 1555-1562	
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SHS	C6	D. Garrett, M. Stan, "Low Power Architecture of the Soft-Output Viterbi Algorithm".	
SHS	C7	Jong Min Kim, Nan Jin Park, "Implementation of Convolutional Encoder and Viterbi Decoder for Wideband CDMA PCS Baseband Processing Unit Using Multiple TMS320C40s".	
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SHS	C11	H. Hendrix, "Viterbi Decoding Techniques in the TMS320C54x Family", Texas Instruments Application Note, June 1996.	

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